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forming a data frame comprising a header field, a payload field, and a framewide parity field, wherein said payload field comprises a first parity subfield;

populating said framewide parity field with at least one parity bit from a first error-control coding scheme that can detect at least *i* bit errors in said data frame, wherein *i* is a positive integer; and populating said first parity subfield with at least one parity bit from a second error-control coding scheme that can correct at least *j* bit errors in said header field, wherein *j* is a positive integer.

- **2.** The method of claim 1 wherein  $j \ge i$ .
- 3. The method of claim 1 wherein said at least one parity bit from said second error-control coding scheme can correct at least *j* bit errors in said payload field.
- 4. The method of claim 1 wherein said second error-control coding scheme is a block error-control coding scheme.
- 5. The method of claim 1 further comprising populating a second parity subfield with at least one parity bit from a third error-control coding scheme that can correct at least k bit errors in said payload field, wherein k is a positive integer.
  - **6.** The method of claim 5 wherein  $k \ge i$ .
  - 7. The method of claim 5 wherein  $j \ge k$ .
  - **8.** The method of claim 5 wherein  $j \ge k \ge i$ .
  - 9. An apparatus comprising:
- a processor for forming a data frame comprising a header field, a payload field, and a framewide parity field, wherein said payload field comprises a first parity subfield, for populating said framewide parity field with at least one parity bit from a first error-control coding scheme that can detect at least i bit errors in said data frame, wherein i is a positive integer, and for populating said first parity subfield with at least one parity bit from a second error-control coding scheme that can correct at least i bit errors in said header field, wherein j is a positive integer; and
- a transmitter for transmitting said data frame onto a shared-bandwidth telecommunication network.
  - 10. The apparatus of claim 9 wherein  $j \ge i$ .

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- 1 11. The apparatus of claim 9 wherein said at least one parity bit from a second error-control coding scheme can correct at least j bit errors in said payload field.
  - 12. The apparatus of claim 9 wherein said second error-control coding scheme is a block error-control coding scheme.
  - 13. The apparatus of claim 9 further comprising populating a second parity subfield with at least one parity bit from a third error-control coding scheme that can correct at least k bit errors in said payload field, wherein k is a positive integer.
    - 14. The apparatus of claim 13 wherein  $k \ge i$ .
    - 15. The apparatus of claim 13 wherein  $j \ge k$ .
    - 16. The apparatus of claim 13 wherein  $j \ge k \ge i$ .
    - 17. A method comprising:

forming a data frame comprising a header field, a payload field, and a framewide parity field, wherein said payload field comprises: a payload data subfield, a first parity subfield, and a second parity subfield;

populating said framewide parity field with at least one parity bit from a first error-control coding scheme that can detect at least i bit errors in said data frame, wherein i is a positive integer;

populating said first parity subfield with at least one parity bit from a second error-control coding scheme that can correct at least j bit errors in said data frame, wherein j is a positive integer; and

populating said second parity subfield with at least one parity bit from a third error-control coding scheme that can correct at least k bit errors in said payload data subfield, wherein k is a positive integer.

- **18.** The method of claim 17 wherein  $j \ge i$ .
- 1 19. The method of claim 17 wherein  $j \ge k$ .
  - **20.** The method of claim 17 wherein  $j \ge k \ge i$ .
  - 21. The method of claim 17 wherein said second error-control coding scheme is a block error-control coding scheme, and wherein said third error-control coding scheme is a block error-control coding scheme.

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